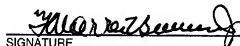


FORM PTO-1390 (REV 11-2000)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTORNEY'S DOCKET NUMBER <b>1275-48</b>
TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371		U.S. APPLICATION NO. (If known, see 37 C.F.R. 1.5) <b>10/031200</b>
INTERNATIONAL APPLICATION NO. <b>PCT/JP00/05167</b>	INTERNATIONAL FILING DATE <b>02/08/2000</b>	PRIORITY DATE CLAIMED <b>03/08/1999</b>
TITLE OF INVENTION <b>PACKET PROCESSOR AND RECORDING MEDIUM WITH PACKET PROCESSING PROGRAMS THEREON</b>		
APPLICANT(S) FOR DO/EO/US <b>NAKABAYASHI, J.</b>		
Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:		
1. <input checked="" type="checkbox"/> This is a <b>FIRST</b> submission of items concerning a filing under 35 U.S.C. 371. 2. <input type="checkbox"/> This is a <b>SECOND</b> or <b>SUBSEQUENT</b> submission of items concerning a filing under 35 U.S.C. 371. 3. <input checked="" type="checkbox"/> This is an express request to begin national examination procedures (35 U.S.C. 371(f)). The submission must include items (5), (6), (9) and (21) indicated below. 4. <input checked="" type="checkbox"/> The U.S. has been elected by the expiration of 19 months from the priority date (Article 31). 5. A copy of the International Application as filed (35 U.S.C. 371(c)(2)). a. <input type="checkbox"/> is attached hereto (required only if not communicated by the International Bureau). b. <input checked="" type="checkbox"/> has been communicated by the International Bureau. c. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US). 6. <input checked="" type="checkbox"/> An English language translation of the International Application as filed (35 U.S.C. 371(c)(2)). a. <input checked="" type="checkbox"/> is attached hereto. b. <input type="checkbox"/> has been previously submitted under 35 U.S.C. 154(d)(4). 7. <input checked="" type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3)). a. <input type="checkbox"/> are attached hereto (required only if not communicated by the International Bureau). b. <input checked="" type="checkbox"/> have been communicated by the International Bureau. c. <input type="checkbox"/> have not been made; however, the time limit for making such amendments has <b>NOT</b> expired. d. <input type="checkbox"/> have not been made and will not be made. 8. <input checked="" type="checkbox"/> An English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)). 9. <input checked="" type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)). 10. <input type="checkbox"/> A English language translation of the annexes of the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).		
Items 11 To 20 below concern document(s) or information included:		
11. <input type="checkbox"/> An Information Disclosure Statement under 37 C.F.R. 1.97 and 1.98. 12. <input checked="" type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 C.F.R. 3.28 and 3.31 is included. 13. <input type="checkbox"/> A FIRST preliminary amendment. 14. <input type="checkbox"/> A SECOND or SUBSEQUENT preliminary amendment. 15. <input type="checkbox"/> A substitute specification. 16. <input type="checkbox"/> A change of power of attorney and/or address letter. 17. <input type="checkbox"/> A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821-1.825. 18. <input type="checkbox"/> A second copy of the published international application under 35 U.S.C. 154(d)(4). 19. <input type="checkbox"/> A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4). 20. <input checked="" type="checkbox"/> Other items or information. PTO Form 1449		

U.S. APPLICATION NO. <b>10/051200</b> UNKNOWN		INTERNATIONAL APPLICATION NO. <b>PCT/JP00/05167</b>		ATTORNEY'S DOCKET NUMBER <b>1275-48</b>	
21. <input checked="" type="checkbox"/> The following fees are submitted:				<b>CALCULATIONS</b> PTO USE ONLY	
<b>BASIC NATIONAL FEE (37 C.F.R. 1.492(a)(1)-(5)):</b> -- Neither international preliminary examination fee (37 C.F.R. 1.482) nor international search fee (37 C.F.R. 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO .....\$1040.00 -- International preliminary examination fee (37 C.F.R. 1.482) not paid to USPTO but International Search Report prepared by the EPO or JPO .....\$890.00 -- International preliminary examination fee (37 C.F.R. 1.482) not paid to USPTO but international search fee (37 C.F.R. 1.445(a)(2)) paid to USPTO .....\$740.00 -- International preliminary examination fee (37 C.F.R. 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1)-(4) .....\$710.00 -- International preliminary examination fee (37 C.F.R. 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1)-(4) .....\$100.00  <div style="text-align: right;"><b>ENTER APPROPRIATE BASIC FEE AMOUNT =</b></div>				<div style="text-align: right;">\$ 890.00</div>	
Surcharge of \$130.00 for furnishing the oath or declaration later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 C.F.R. 1.492(e)).				\$ 0.00	
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE		
Total Claims	24	-20 =	4	X	\$18.00
Independent Claims	24	-3 =	21	X	\$84.00
MULTIPLE DEPENDENT CLAIMS(S) (if applicable)					\$280.00
<b>CLAIM FEES ARE NOT BEING PAID AT THIS TIME</b>				<b>TOTAL OF ABOVE CALCULATIONS = \$ 2726.00</b>	
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27. The fees indicated above are reduced by 1/2.					0.00
<b>SUBTOTAL =</b>				<b>\$ 2726.00</b>	
Processing fee of \$130.00, for furnishing the English Translation later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 C.F.R. 1.492(f)).				0.00	
<b>TOTAL NATIONAL FEE =</b>				<b>\$ 2726.00</b>	
Fee for recording the enclosed assignment (37 C.F.R. 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 C.F.R. 3.28, 3.31). <b>\$40.00</b> per property				40.00	
Fee for Petition to Revive Unintentionally Abandoned Application (\$1280.00 - Small Entity = \$640.00)				0.00	
<b>TOTAL FEES ENCLOSED =</b>				<b>\$ 2766.00</b>	
				Amount to be: refunded	\$
				Charged	\$
a) <input checked="" type="checkbox"/> A check in the amount of \$2766.00 to cover the above fees is enclosed. b) <input type="checkbox"/> Please charge my Deposit Account No. 14-11410 in the amount of \$_____ to cover the above fees. A duplicate copy of this form is enclosed. c. <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 14-1140. A <u>duplicate</u> copy of this form is enclosed. d. <input checked="" type="checkbox"/> The entire content of the foreign application(s), referred to in this application is/are hereby incorporated by reference in this application.					
<b>NOTE: Where an appropriate time limit under 37 C.F.R. 1.494 or 1.495 has not been met, a petition to revive (37 C.F.R. 1.137(a) or (b)) must be filed and granted to restore the application to pending status.</b>					
<b>SEND ALL CORRESPONDENCE TO:</b>  NIXON & VANDERHYTE P.C. 1100 North Glebe Road, 8 <sup>th</sup> Floor Arlington, Virginia 22201-4714 Telephone: (703) 816-4000					
				 SIGNATURE	
				<b>H. Warren Burnam, Jr.</b> NAME	
				<b>29,366</b> REGISTRATION NUMBER	
				<b>January 15, 2002</b> Date	

DESCRIPTION

PACKET PROCESSOR AND RECORDING MEDIUM WITH PACKET PROCESSING

PROGRAMS THEREON

Technical Field

5 The present invention relates to a packet processor and  
 a recording medium with packet processing programs thereon,  
 which are suitable for terminals and gateway devices for  
 communicating voice and music by way of packet-based networks  
 such as the Internet, etc.

10 Background Art

Voice communication services, represented by telephone,  
 have been conventionally offered by telephone communications  
 companies establishing communications networks on their own  
 15 accounts.

Fig.13 shows an example of a communications network for  
 offering conventional telephone services. The telephone  
 network is comprised of regional centers 801 at the topmost  
 layer, primary centers 802 connected to one regional center,  
 20 toll centers 803 and terminal offices 804, each of which is  
 connected to subscribers 805 as the users. Regional centers  
 801 form mesh networks while primary centers 802, toll centers  
 803 and terminal offices 804 form star-like networks.

In the telephone network, each of the centers and offices  
 25 has an exchange, and when a transmission link needs to be

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established between exchanges, line switching is performed by time division multiplex system of the communication band necessary for the connection. Fig.14 shows the concept of line switching based on time division multiplex system. A telephone 1001 used by a user is connected to an exchange 1002. A transmission path 1003 having a predetermined band is provided to connect between the exchanges. The calls among a plurality of telephones 1001a to 1001h are realized through the transmission paths based on time division multiplex system. It is understood from Fig.14 that calls are established between phone 1001a and phone 1001d, phone 1001b and phone 1001e, and phone 1001c and phone 1001h while the transmission path between the exchanges still has empty areas. In time division multiplex system, since a multiple number of signals to be exchanged between terminals are multiplexed using frames 1004 each having a predetermined period unit, the band for a call once established between terminals will be secured until the call terminates. The unit of frame 1004 of division multiplexing is typically 8 KHz (125  $\mu$ sec.). Since this relation is held for all the connected exchanges, there is no need to pay attention to synchronization between the terminals if each telephone terminal transmits and receives data based on this signal.

In this way, in the line switching network used in the telephone network or the like, the entire network is operated

based on the same reference signal, so that the bandwidth and delay time between connected terminals are guaranteed. On the other hand, because of the spread of PCs and the Internet, communications through electronic mail or through WWW(world wide web) have become intensively developed. Fig.15 shows an example of communications among PCs over the Internet. On the Internet, all information is exchanged in packets. PCs 904a and 904b connected within an intranet 901 is connected to the Internet 905 at an Internet provider 903a by way of a router or gateway 902. Ordinary users of PCs 904c and 904d access to the Internet 905 by way of Internet Providers 903b and 903c using the telephone line via PPP.

Communications on the Internet are performed using TCP/UDP/IP. Fig.16 shows the concept for routing data on the Internet. Each terminal monitors the status of the network and sends out packets having a destination address(IP address) attached thereon onto the network when the network has any empty channel. Packets from terminals connected to the network are checked as to their IP addresses and routed by routers so that they will be transferred to the routers that are located nearest to their destinations. The packets are thus transferred to those routers, where they are checked as to their addresses, and further, transferred to associated terminals.

In this way, data communication through the Internet

by routing makes it possible to transmit and receive data as long as there is an empty channel allowing for packet transmission on the network, so that a large amount of data can be communicated at low cost.

5           In recent years, there has been an increasing tendency toward using applications of the Internet for real-time operations such as IP phone (VoIP), teleconference, IP/TV, etc., in addition to use of non-real-time data communications such as electronic mail and WWW. When the Internet is used  
10       in this manner, the problem of packet jitter due to routing occurs.

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15           The situation of occurrence of jitter will be explained with reference to Fig.16. Packets 1102a and 1102b sent out respectively from PCs 1101a and 1101b reach a router 1103a. In order to transfer the packets to their own destinations, router 1103a sends out packets 1102a and 1102b in the order  
15       in which they reached it. No jitter will arise if the packets from each terminal just fit in the transmission intervals between the packets from the other terminal. However, if two  
20       packets are sent out at almost the same timing, the packet which has first arrived at router 1103a is processed first while the packet from the other terminal is kept waiting during that time. For example, as shown in Fig.16, suppose that  
25       packets 1102a are sent out to router 1103a at intervals of period 1104a while packets 1102b are sent out at intervals

of period 1104b. Router 1103a processes the packets and sends them out to the router 1103b in the order in which they reached it. First, packet 1102b and then packet 1102a are processed. Therefore, packet 1102a is kept waiting from its arrival until the process of packet 1102b is completed. As a result, the transmission intervals become different from those at which the packets were send out from PC1101a and 1101b. At router 1130b, packets 1102a and 1102b are separated and sent out to respective destinations PC1101c and 1101d. That is, the transmission interval of packets 1102a changes from 1104a to 1104c and the transmission interval of packets 1102b changes from 1104b to 1104d, which will cause jitter.

As stated above, packet communication such as through the Internet causes packet jitter due to routing over the network. When packet jitter occurs, voice sound comes in with breaks in the case of IP phone, for example. For improvement against this, in general a buffer is provided on the receiver side so that data can be reproduced after a certain amount of data has been stored. However, since jitter on the network depends on the traffic in the network during the communication, breaks occur if the buffer has a lower storage capacity whereas delay increases if the buffer has a higher storage capacity, degrading the characteristics of conversation on IP phone.

Another problem with packet communication is the difference in clock rate between the transmitter end and the

receiver end. The problem with real-time operations in packet communication will be described with reference to Fig. 17. A transmitting terminal 1202a is comprised of a microphone 1211, an A/D converting circuit 1212, an encoder circuit 1213, a network interface 1214a while a receiving terminal 1202b is comprised of a network interface 1214b, a decoder circuit 1215, an D/A converting circuit 1216 and a speaker 1217. In packet communication, since each terminal does not operate in synchronism with the clock on the network as in a line exchange configuration, individual terminals operate in accordance with their own clocks 1201a and 1201b, respectively. Here, if there is a difference in clock rate for sampling voice sound between the transmitter side and receiver side, data overflow or underflow will occur on the receiver side.

In order to solve this, in the packet communication, there is a method of reproducing the reference clock, which is used for audio and video transmission based on ATM in MPEG2. The overall configuration will be described with reference to Fig. 18. Also in ATM, data is transmitted in packets (cells) as in the Internet mentioned above. In MPEG2 transmission based on ATM, a 27 MHz clock 1301 is provided for the terminal so as to transmit data together with reference clock information 1302 as clock reference signal information (PCR) 1303. The receiving terminal reproduces the data using a PLL 1304 based on the clock reference signal information (PCR)



1303. With this arrangement, the reference clock information 1302 on the transmitter side can be reproduced on the receiver side so that no buffer overflow and underflow will occur due to clock discrepancy.

5 This method is markedly effective as a method of sending a reference clock to a destination terminal by packet communication having no common clock, but needs to provide a 27 MHz clock on the transmitter side and a high-precision PLL on the receiver side, which are too expensive to be provided for PCs and the like. Further, it is necessary for this method to send an exact PCR from the transmitter side, and this method is not effective for connection with a terminal which cannot send this information exactly.

10 In order to solve the above problems, it is therefore an object of the present invention to provide an inexpensive packet processor and a recording medium with packet communication processing programs recorded thereon, wherein no receiving buffer overflow and underflow due to clock discrepancy between the transmission and reception ends will occur so as to prevent occurrence of packet jitter and hence voice sound with breaks.

#### Disclosure of Invention

15 In order to solve the above problems, the present invention is configured as follows:

The first invention is a packet processor for processing packets of data received via a network to reproduce the data, comprising: a receiving buffer for storing received packets; a reproducing means for reproducing data of the packets based on a reproduction clock; and a control means for performing control so as to reproduce data without breaks, and is characterized in that the control means controls the packets having been stored in the receiving buffer by their number, holds a reference value for the number of received packets, which is determined beforehand, and causes the reproducing means to start data reproduction when the number of packets exceeds the reference number.

The second invention is the packet processor defined in the first invention, wherein the control means monitors the state of the receiving buffer and collects, when the data in the buffer is used up, data up to the level corresponding to the reference value before causing the reproducing means to restart data reproduction.

The third invention is the packet processor defined in the first invention, wherein the control means monitors the state of the receiving buffer and discards, when the data in the buffer exceeds the predetermined buffer size, a fixed amount of data from the buffer.

The fourth invention is the packet processor defined in the first invention, wherein the control means monitors

the state of the receiving buffer and modifies, when the data in the buffer is used up, the reference value for the number of received packet at which reproduction of data is started, upon the next reproduction of data.

5           The fifth invention is the packet processor defined in the first invention, wherein the control means monitors the state of the receiving buffer and modifies, when the data in the buffer exceeds the predetermined buffer size, the size of the receiving buffer.

10           The sixth invention is the packet processor defined in any one of the second through fifth inventions, wherein the control means monitors the number of packets in the receiving buffer at intervals of a predetermined time period, and when the result shows increase or decrease in the number of packets with time, the control means, based on the result, modifies the receiving buffer size and the reference value for the number of received packets at which reproduction of data is started.

15           The seventh invention is the packet processor defined in any one of the second through fifth inventions, wherein the control means monitors the number of packets in the receiving buffer at intervals of a predetermined time period, and when the result shows increase or decrease in the number of packets with time, the control means, based on the result, modifies the clock for data reproduction.

The eighth invention is the packet processor defined in the fourth or fifth invention, wherein the control means records the modifications of the reference value for the number of received packets or the receiving buffer size, continuously from the start of the communication, and if modifications of one type successively occur, the control means, based on the result, modifies the receiving buffer size and the reference value for the number of received packets at which reproduction of data is started.

The ninth invention is the packet processor defined in the fourth or fifth invention, wherein the control means records the modifications of the reference value for the number of received packets or the receiving buffer size, continuously from the start of the communication, and if modifications of one type successively occur, the control means, based on the result, modifies the clock for data reproduction.

The tenth invention is a recording medium holding a packet processing program for use in a packet processor for processing packets of data received via a network to reproduce the data, wherein the packet processor comprises a receiving buffer for storing received packets; a reproducing means for reproducing data of the packets based on a reproduction clock; and a control means for performing control so as to reproduce data without breaks, and the packet processing program is characterized in that the control means is adapted to control

the packets having been stored in the receiving buffer by their number, hold with a reference value for the number of received packets, which is determined beforehand, and cause the reproducing means to start data reproduction when the number of packets exceeds the reference value.

The eleventh invention is the recording medium holding a packet processing program defined in the tenth invention, wherein the control means is adapted to monitor the state of the receiving buffer and collect, when the data in the buffer is used up, data up to the level corresponding to the reference value before causing the reproducing means to restart data reproduction.

The twelfth invention is the recording medium holding a packet processing program defined in the tenth invention, wherein the control means is adapted to monitor the state of the receiving buffer and discard, when the data in the buffer exceeds the predetermined buffer size, a fixed amount of data from the buffer.

The thirteenth invention is the recording medium holding a packet processing program defined in the tenth invention, wherein the control means is adapted to monitor the state of the receiving buffer and modify, when the data in the buffer is used up, the reference value for the number of received packet at which reproduction of data is started, upon the next reproduction of data.

The fourteenth invention is the recording medium holding a packet processing program defined in the tenth invention, wherein the control means is adapted to monitor the state of the receiving buffer and modify, when the data in the buffer exceeds the predetermined buffer size, the size of the receiving buffer.

The fifteenth invention is the recording medium holding a packet processing program defined in any one of the eleventh through fourteenth inventions, wherein the control means is adapted to monitor the number of packets in the receiving buffer at intervals of a predetermined time period and when the result shows increase or decrease in the number of packets with time, the control means is operated to modify, based on the result, the receiving buffer size and the reference value for the number of received packets at which reproduction of data is started.

The sixteenth invention is the recording medium holding a packet processing program defined in any one of the eleventh through fourteenth inventions, wherein the control means is adapted to monitor the number of packets in the receiving buffer at intervals of a predetermined time period and when the result shows increase or decrease in the number of packets with time, the control means is operated to modify, based on the result, the clock for data reproduction.

The seventeenth invention is the recording medium holding

a packet processing program defined in the thirteenth or  
fourteenth invention, wherein the control means is adapted  
to record the modifications of the reference value for the  
number of received packets or the receiving buffer size,  
continuously from the start of the communication, and if  
modifications of one type successively occur, the control  
means is operated to modify, based on the result, the receiving  
buffer size and the reference value for the number of received  
packets at which reproduction of data is started.

The eighteenth invention is the recording medium holding  
a packet processing program defined in the thirteenth or  
fourteenth invention, wherein the control means is adapted  
to record the modifications of the reference value for the  
number of received packets or the receiving buffer size,  
continuously from the start of the communication, and if  
modifications of one type successively occur, the control  
means is adapted to modify, based on the result, the clock  
for data reproduction.

It is expected that real-time applications using the  
Internet such as IP phones etc., will become more and more  
popular. Use of the packet processor of the present invention  
provides a simple method which can realize receiving buffer  
control for reproducing data such as voice sound, music etc.,  
in an optimal way by absorbing packet jitter which varies  
over time depending on the network status. Since none of the

temporal information from the transmitting side, which has been used in the conventional configuration, is used in the packet processor of the present invention, the optimal control can be achieved on the receiving side only.

Also, since difference in reference clocks between the terminal ends, which may cause a problem in packet communication, can be detected by only monitoring the status of the receiving buffer at regular intervals or when a particular event has occurred, it is possible to modify the difference in a simple, low-cost method without the necessity of a complex, expensive PLL circuit and/or high-precision reference clock.

Use of a recording medium with the packet processing program of the present invention recorded thereon enables jitter of received packets and clock difference, which could only be handled by the terminals with dedicated hardware, to be controlled in software by multi-purpose terminals such as PCs and the like.

#### Brief Description of Drawings

Fig.1 is a block diagram showing a communication system made up of voice packet communication apparatuses of the present invention; Fig.2 is block diagram showing a voice processor used this communication apparatus; Fig.3 is a block diagram showing another voice processor used in this



communication apparatus; Fig.4 is an illustrative view showing the operation of a receiving buffer; Fig.5 is a flowchart for illustrating the operation of a reproduction controller controlling a receiving buffer; Fig.6 is an illustrative view showing the states of a receiving buffer in a time-sequential manner; Fig.7 is an illustrative view showing the state of received packets and a receiving buffer in a time-sequential manner; Fig.8 is an illustrative view showing another state of received packets and a receiving buffer in a time-sequential manner; Fig.9 is a flowchart for illustrating a method of detecting the difference between reference clocks on terminal ends; Fig.10 is an illustrative view showing an example of a determining method when the status of a receiving buffer is monitored every fixed period; Fig.11 is a flowchart for detecting the difference when a difference in reference clocks occurs; Fig.12 is another example of a determining method when the status of a receiving buffer is monitored every fixed period; Fig.13 is an illustrative view showing an example of communications networks for providing conventional telephone services; Fig.14 is an illustrative view showing the concept of line switching based on time division multiplex system; Fig.15 is an illustrative view showing an example of communication between PCs using the Internet; Fig.16 is an illustrative view showing the concept when data is routed over the Internet; Fig.17 is a block diagram showing a

conventional packet communication apparatus where there is a difference in clocks between the transmitting end and receiving end; and Fig.18 is a block diagram showing a packet communication apparatus having a function of reproducing the reference clock, which is used for video and audio transmission over ATM based on MPEG2.

#### Best Mode for Carrying Out the Invention

The embodiment of the present invention will be described hereinbelow with reference to the drawings.

Fig.1 is a block diagram showing a communication system of voice packet communication apparatus of the present invention, Figs.2 and 3 are block diagrams showing voice processors used in this communication system.

The communication system shown in Fig.1 comprises voice packet communication apparatus 100a and 100b and the Internet 105. Voice packet communication apparatus (terminal units) 100a and 100b are comprised of microphones 101a and 101b, speakers 102a and 102b, voice processors 103a and 103b serving as packet processors and network interfaces 104a and 104b. A voice sound input through microphone 101a is A/D converted through voice processor 103a and encoded as required, then packetized so that the packets are transmitted from network interface 104a to terminal unit 100b by way of Internet 105. The packets sent out from terminal unit 100a are processed

through network interface 104b and buffered in voice processor 103b, then decoded as required, and D/A converted so as to be output from speaker 102b.

The configurations of voice processor 103 are shown in detail in Figs.2 and 3. Fig.2 shows a configuration for controlling the receiving buffer to reproduce voice data in an optimal way so as to absorb the jitter. Fig.3 shows a configuration for modifying the clock for reproduction based on the state of the receiving buffer so as to absorb the difference in clocks between terminal ends. Voice processor 103 comprises a reproduction controller 106, a clock generator 107, a receiving buffer 108, a decoder 109 and a D/A converter 110. Decoder 109 and D/A converter 110 function as a data reproducing means to reproduce data in packets.

In Fig.2, packets received from network interface 104 are stored into receiving buffer 108. Reproduction controller 106 checks the state of receiving buffer 108 and performs a reproduction process mentioned below. The data in receiving buffer 108 is transferred to decoder 109 in accordance with instruction from reproduction controller 106 and decoded as voice sound, D/A converted by D/A converter 110 and then output as voice sound from speaker 102. The clock for D/A conversion is supplied from a reproduction clock CLK 107.

Fig.3 shows a configuration which, in addition to the

control shown in Fig.2, decodes voice sound by modifying the frequency of reproduction clock CLK 107 in accordance with instruction from reproduction controller 106 so that data of received packets to be reproduced will not cause overflow or underflow.

Fig.4 is an illustrative view showing the operation of the receiving buffer. Fig.5 is a flowchart for illustrating the operation of the reproduction controller.

In Fig.4,  $N$  designates the size of receiving buffer 108,  $n$  designates the reference value for received packets at which the received data starts to be reproduced, and  $x$  designates the number of packets in receiving buffer 108.

Packets 201 sent from network interface 104 are temporarily stored into receiving buffer 108, and transferred to decoder 109 in accordance with instruction from reproduction controller 106 so that the data is reproduced. In the flowchart in Fig.5, packets are retained in receiving buffer 108 until  $x=n$  (S301). When  $x=n$ , reproduction of data is started (S302). Then reproduction controller 106 monitors the status of receiving buffer 108 and continues receiving packets and reproducing the data unless the relation  $x=0$  (S303) or  $x=N$  (S304) holds.

Fig.6 is an illustrative view showing the state of the receiving buffer in a time-sequential manner. As to the initial value of  $n$ , the operation should be started with  $n$

set at a value as small as 1 or 2 if the condition of the network is unknown. If the value which would cause jitter can be predicated beforehand, the initial value of  $n$  should be determined based on that value.

5 Referring next to the flowchart in Fig.5, the operation when receiving buffer 108 has become empty so that  $x=0$  (S303) will be described. This condition indicates the occurrence of underflow caused by the fact that arrivals of packets are delayed with respect to the reproduction clock due to  
10 network-induced jitter or difference in clocks between the terminal ends.

Therefore, prevention of the occurrence of underflow is attempted by increasing the number of packets to be held in receiving buffer 108. When  $x=0$  (S303), reproduction  
15 controller 106 collects packets into receiving buffer 108, stopping reproduction (S305) and increments the reference value for the number of received packets at which reproduction is started, or sets  $n=n+1$  (S306). When the number of the packets in receiving buffer 108 becomes equal to  $n$  (S301),  
20 reproduction is started. The state of receiving buffer 108 in this case is shown in Fig.4(b). Here, it is understood that  $n$  is incremented when receiving buffer 108 has become empty. Fig.7 illustrates this situation in a time-sequential manner. Because of delay of the arrival of received packets  
25 201 due to some reason, the buffer becomes empty(501),  $n$  is

incremented ( $n=n+1$ ) and reproduction is stopped (503). When  $x=n$ , reproduction is started (504).

Referring next to the flowchart in Fig.5, the operation when  $x$  becomes equal to  $N$  in receiving buffer 108 (S304) will be described. This condition indicates the occurrence of overflow caused by the fact that packets have arrived faster than the clock on the reproduction side due to network-induced jitter or difference in clocks between terminal ends. Therefore, the occurrence of overflow is prevented by increasing the buffer size. When  $x=N$  (S304), reproduction controller 106 stops reception of packets and discards part of the packets held in receiving buffer 108 so that the state of buffer 108 is set at  $x=n$  (S307). Then, the receiving buffer size  $N$  is incremented ( $N=N+1$ ) (S308). During this period, reproduction is continued and reception of packets is started again. Fig.4(c) shows the state of receiving buffer 108 in this situation. It is understood that packets are discarded and the receiving buffer size  $N$  is incremented after the buffer has become full. Fig.8 is an illustration for showing this situation in a time-sequential manner. Because of the early arrival of received packets 201 for some reason, buffer 108 becomes full (505), and after setting  $x=n$  and  $N=N+1$  (506), reception of packets is restarted (507).

The reproduction control of the receiving buffer as above enables suitable reproduction control based on the number

of received packets even if an overflow or underflow arises due to network-induced jitter or difference in reference clocks between terminal ends.

Fig.9 is a flowchart for illustrating a method of detecting the difference in reference clocks between terminal ends, used in the present invention. Fig.10 shows illustrative charts for showing an example of the determining method when the state of the receiving buffer is monitored every fixed period.

In Fig.9, packets are accumulated into receiving buffer 108 until  $x=n$  (S301). When  $x=n$ , then reproduction of the data is started (S302). Reproduction controller 106 monitors the number of packets in receiving buffer 108 every fixed period (S401) and plots the number (S402). Fig.10 illustrates the plot results and the determining method. In Fig.10(a), though the packet count  $x$  is found to vary to some extent, this variation is attributed to network induced jitter, so that it is possible to determine that no difference in frequency between the terminal ends. Fig.10(b) shows decrease in the number of packets in the buffer due to frequency difference in addition to the variation due to jitter. Fig.10(c) shows increase in the number of packets in the buffer due to frequency difference in addition to the variation due to jitter. When it is found from the plot result judgment (S403) that the number of packets in the buffer decreases due to frequency

difference,  $n$  is incremented ( $n=n+1$ ) (S404) and the frequency of the reference clock is varied so as to become smaller (S405). When increase in the number of packets in the buffer due to frequency difference is found from the plot result,  $n$  is decremented ( $n=n-1$ ) (S406) and the frequency of the reference clock is varied so as to become greater (S407).

As to changing the value ' $n$ ' and changing the reference clock frequency from the plot result, changing one of them only is still effective.

Fig.11 shows the flow of plotting the number of events in the horizontal axis and a variable  $Y$  which represents the state of occurrence of events in the vertical axis when an event ' $x=0$ ' (S408) or ' $x=N$ ' (S409) occurs in the reproduction control of this invention. The plot starts from zero in the horizontal axis as shown in Figs.7 and 8 and increases by one every occurrence of an event. As to the vertical axis, the plot starts from  $Y=0$ , and variable  $Y$  is changed so that  $Y=Y-1$  (S410) for  $n=0$  (S408) and  $Y=Y+1$  (S413) for  $n=N$  (S409) every time an event ' $x=0$ ' or ' $x=N$ ' occurs. The plot result and the determining method are shown in Fig.7. In Fig.12(a), no particular tendency is found as to occurrence of the events, and the occurrence of the events can be attributed to network induced jitter. Hence, it can be determined that there is no difference in frequency between the terminal ends. In Fig.12(b), it is understood that there is a tendency toward

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underflow due to difference in frequency in addition to the fluctuation caused by jitter. In Fig.12(c), it is understood that there is a tendency toward overflow due to difference in frequency in addition to the fluctuation caused by jitter. The operation of setting  $Y=Y-1$  (S502) when  $x=0$  is shown in Fig.7, and the operation of setting  $Y=Y+1$  (S504) when  $x=N$  is shown in Fig.8.

From the result determination (S412 and S415) by plotting (S411 and S414) based on the above operations, the difference in frequency between the terminal ends may be corrected by changing the value 'n', modifying the frequency of the reference clock, or adjusting both in the same manner as described with reference to Figs.9 and 10.

As has been described, use of a packet processor of the present invention provides a simple method which can realize reception buffer control for reproducing data such as voice sound etc., in an optimal way by absorbing packet jitter which varies over time depending on the network status. Since non of temporal information from the transmitting side, which has been used in the conventional configuration, is used in the method of the present invention, the optimal control can be achieved on the receiving side only.

Also, since difference in reference clocks between the terminal ends, which may cause a problem in packet communication, can be detected by only monitoring the status

of the receiving buffer when a particular event has occurred, it is possible to modify the difference in a simple, low-cost method without the necessity of a complex, expensive PLL circuit and/or high-precision reference clock.

5           Use of a recording medium in which the packet processing program of the present invention is stored enables jitter of received packets and clock difference, which could only be handled by the terminals with dedicated hardware, to be controlled in software by multi-purpose terminals such as PCs and the like.

#### 10           Industrial Applicability

15           As has been described heretofore, the packet processor according to the present invention is suitable for use with terminals and gateways which communicate voice sound and music over a packet-based network such as the Internet.

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16. (deleted)

17. (deleted)

18. (deleted)

19. (added)      A packet processor for processing packets of  
5 data received via a network to reproduce the data, comprising:

    a receiving buffer for storing received packets;

    a reproducing means for reproducing data of the packets  
based on a reproduction clock; and

    a control means for performing control so as to reproduce  
10 data without breaks,

    wherein the control means controls the packets having  
been stored in the receiving buffer by their number, holds  
a reference value for the number of received packets, which  
is determined beforehand, and causes the reproducing means  
15 to start data reproduction when the number of packets exceeds  
the reference number; monitors a state of the receiving buffer  
and collects, when the data in the buffer is used up, data  
up to a level corresponding to the reference value before  
causing the reproducing means to restart data reproduction;  
20 and monitors the number of packets in the receiving buffer  
at intervals of a predetermined time period, and when a result  
shows increase or decrease in the number of packets with time,  
the control means, based on the result, modifies a receiving  
buffer size and the reference value for the number of received  
25 packets at which reproduction of data is started.

20. (added) A packet processor for processing packets of data received via a network to reproduce the data, comprising:

a receiving buffer for storing received packets;

a reproducing means for reproducing data of the packets based on a reproduction clock; and

a control means for performing control so as to reproduce data without breaks,

wherein the control means controls the packets having been stored in the receiving buffer by their number, holds a reference value for the number of received packets, which is determined beforehand, and causes the reproducing means to start data reproduction when the number of packets exceeds the reference number; monitors a state of the receiving buffer and discards, when the data in the buffer exceeds a predetermined buffer size, a fixed amount of data from the buffer; and monitors the number of packets in the receiving buffer at intervals of a predetermined time period, and when a result shows increase or decrease in the number of packets with time, the control means, based on the result, modifies the receiving buffer size and the reference value for the number of received packets at which reproduction of data is started.

21. (added) A packet processor for processing packets of

data received via a network to reproduce the data, comprising:

a receiving buffer for storing received packets;

a reproducing means for reproducing data of the packets based on a reproduction clock; and

5 a control means for performing control so as to reproduce data without breaks,

wherein the control means controls the packets having been stored in the receiving buffer by their number, holds a reference value for the number of received packets, which  
10 is determined beforehand, and causes the reproducing means to start data reproduction when the number of packets exceeds the reference number; monitors a state of the receiving buffer and modifies, when the data in the buffer is used up, the reference value for the number of received packet at which  
15 reproduction of data is started, upon a next reproduction of data; and monitors the number of packets in the receiving buffer at intervals of a predetermined time period, and when a result shows increase or decrease in the number of packets with time, the control means, based on the result, modifies  
20 a receiving buffer size and the reference value for the number of received packets at which reproduction of data is started.

22. (added) A packet processor for processing packets of data received via a network to reproduce the data, comprising:

25 a receiving buffer for storing received packets;

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a reproducing means for reproducing data of the packets based on a reproduction clock; and

a control means for performing control so as to reproduce data without breaks,

5            wherein the control means controls the packets having been stored in the receiving buffer by their number, holds a reference value for the number of received packets, which is determined beforehand, and causes the reproducing means to start data reproduction when the number of packets exceeds the reference number; monitors a state of the receiving buffer and modifies, when the data in the buffer exceeds a  
10            predetermined buffer size, the size of the receiving buffer; and monitors the number of packets in the receiving buffer at intervals of a predetermined time period, and when a result  
15            shows increase or decrease in the number of packets with time, the control means, based on the result, modifies the receiving buffer size and the reference value for the number of received packets at which reproduction of data is started.

20            23. (added)    A packet processor for processing packets of data received via a network to reproduce the data, comprising:

            a receiving buffer for storing received packets;

            a reproducing means for reproducing data of the packets based on a reproduction clock; and

25            a control means for performing control so as to reproduce

data without breaks,

wherein the control means controls the packets having been stored in the receiving buffer by their number, holds a reference value for the number of received packets, which is determined beforehand, and causes the reproducing means to start data reproduction when the number of packets exceeds the reference number; monitors a state of the receiving buffer and collects, when the data in the buffer is used up, data up to a level corresponding to the reference value before causing the reproducing means to restart data reproduction; and monitors the number of packets in the receiving buffer at intervals of a predetermined time period, and when a result shows increase or decrease in the number of packets with time, the control means, based on the result, modifies a clock for data reproduction.

24. (added) A packet processor for processing packets of data received via a network to reproduce the data, comprising:

a receiving buffer for storing received packets;  
a reproducing means for reproducing data of the packets based on a reproduction clock; and  
a control means for performing control so as to reproduce data without breaks,

wherein the control means controls the packets having been stored in the receiving buffer by their number, holds

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a reference value for the number of received packets, which is determined beforehand, and causes the reproducing means to start data reproduction when the number of packets exceeds the reference number; monitors a state of the receiving buffer and discards, when the data in the buffer exceeds a predetermined buffer size, a fixed amount of data from the buffer; and monitors the number of packets in the receiving buffer at intervals of a predetermined time period, and when a result shows increase or decrease in the number of packets with time, the control means, based on the result, modifies a clock for data reproduction.

25. (added) A packet processor for processing packets of data received via a network to reproduce the data, comprising:

a receiving buffer for storing received packets;  
a reproducing means for reproducing data of the packets based on a reproduction clock; and  
a control means for performing control so as to reproduce data without breaks,

wherein the control means controls the packets having been stored in the receiving buffer by their number, holds a reference value for the number of received packets, which is determined beforehand, and causes the reproducing means to start data reproduction when the number of packets exceeds the reference number; monitors a state of the receiving buffer



and modifies, when the data in the buffer is used up, the reference value for the number of received packet at which reproduction of data is started, upon a next reproduction of data; and monitors the number of packets in the receiving buffer at intervals of a predetermined time period, and when a result shows increase or decrease in the number of packets with time, the control means, based on the result, modifies a clock for data reproduction.

26. (added) A packet processor for processing packets of data received via a network to reproduce the data, comprising:

a receiving buffer for storing received packets;

a reproducing means for reproducing data of the packets based on a reproduction clock; and

a control means for performing control so as to reproduce data without breaks,

wherein the control means controls the packets having been stored in the receiving buffer by their number, holds a reference value for the number of received packets, which is determined beforehand, and causes the reproducing means to start data reproduction when the number of packets exceeds the reference number; monitors a state of the receiving buffer and modifies, when the data in the buffer exceeds a predetermined buffer size, the size of the receiving buffer; and monitors the number of packets in the receiving buffer

at intervals of a predetermined time period, and when a result shows increase or decrease in the number of packets with time, the control means, based on the result, modifies a clock for data reproduction.

5

27. (added) A packet processor for processing packets of data received via a network to reproduce the data, comprising:

a receiving buffer for storing received packets;

10

a reproducing means for reproducing data of the packets based on a reproduction clock; and

a control means for performing control so as to reproduce data without breaks,

15

wherein the control means controls the packets having been stored in the receiving buffer by their number, holds a reference value for the number of received packets, which is determined beforehand, and causes the reproducing means to start data reproduction when the number of packets exceeds the reference number; monitors a state of the receiving buffer and modifies, when the data in the buffer is used up, the reference value for the number of received packet at which reproduction of data is started, upon a next reproduction of data; and records the modifications of the reference value for the number of received packets or a receiving buffer size, continuously from a start of a communication, and if

20

25

modifications of one type successively occur, the control

means, based on the result, modifies the receiving buffer size and the reference value for the number of received packets at which reproduction of data is started.

5        28. (added)     A packet processor for processing packets of data received via a network to reproduce the data, comprising:

         a receiving buffer for storing received packets;

         a reproducing means for reproducing data of the packets based on a reproduction clock; and

10        a control means for performing control so as to reproduce data without breaks,

         wherein the control means controls the packets having been stored in the receiving buffer by their number, holds a reference value for the number of received packets, which  
15        is determined beforehand, and causes the reproducing means to start data reproduction when the number of packets exceeds the reference number; monitors a state of the receiving buffer and modifies, when the data in the buffer exceeds a predetermined buffer size, the size of the receiving buffer;  
20        and records the modifications of the reference value for the number of received packets or a receiving buffer size, continuously from a start of a communication, and if modifications of one type successively occur, the control means, based on the result, modifies the receiving buffer  
25        size and the reference value for the number of received packets

at which reproduction of data is started.

29. (added) A packet processor for processing packets of data received via a network to reproduce the data, comprising:

5 a receiving buffer for storing received packets;

a reproducing means for reproducing data of the packets based on a reproduction clock; and

a control means for performing control so as to reproduce data without breaks,

10 wherein the control means controls the packets having been stored in the receiving buffer by their number, holds a reference value for the number of received packets, which is determined beforehand, and causes the reproducing means to start data reproduction when the number of packets exceeds  
15 the reference number; monitors a state of the receiving buffer and modifies, when the data in the buffer is used up, the reference value for the number of received packet at which reproduction of data is started, upon a next reproduction of data; and records the modifications of the reference value  
20 for the number of received packets or a receiving buffer size, continuously from a start of a communication, and if modifications of one type successively occur, the control means, based on the result, modifies a clock for data reproduction.

30. (added) A packet processor for processing packets of data received via a network to reproduce the data, comprising:

a receiving buffer for storing received packets;

a reproducing means for reproducing data of the packets based on a reproduction clock; and

a control means for performing control so as to reproduce data without breaks,

wherein the control means controls the packets having been stored in the receiving buffer by their number, holds a reference value for the number of received packets, which is determined beforehand, and causes the reproducing means to start data reproduction when the number of packets exceeds the reference number; monitors a state of the receiving buffer and modifies, when the data in the buffer exceeds a predetermined buffer size, the size of the receiving buffer; and records the modifications of the reference value for the number of received packets or the receiving buffer size, continuously from a start of a communication, and if modifications of one type successively occur, the control means, based on the result, modifies a clock for data reproduction.

31. (added) A recording medium holding a packet processing program for use in a packet processor for processing packets of data received via a network to reproduce the data, wherein

the packet processor comprises a receiving buffer for storing received packets; a reproducing means for reproducing data of the packets based on a reproduction clock; and a control means for performing control so as to reproduce data without breaks, and

wherein the packet processing program is characterized in that the control means is adapted to control the packets having been stored in the receiving buffer by their number, hold with a reference value for the number of received packets, which is determined beforehand, and cause the reproducing means to start data reproduction when the number of packets exceeds the reference value; the control means is adapted to monitor a state of the receiving buffer and collect, when the data in the buffer is used up, data up to a level corresponding to the reference value before causing the reproducing means to restart data reproduction; and the control means is adapted to monitor the number of packets in the receiving buffer at intervals of a predetermined time period and when a result shows increase or decrease in the number of packets with time, the control means is operated to modify, based on the result, a receiving buffer size and the reference value for the number of received packets at which reproduction of data is started.

32. (added) A recording medium holding a packet processing

program for use in a packet processor for processing packets of data received via a network to reproduce the data, wherein the packet processor comprises a receiving buffer for storing received packets; a reproducing means for reproducing data of the packets based on a reproduction clock; and a control means for performing control so as to reproduce data without breaks, and

wherein the packet processing program is characterized in that the control means is adapted to control the packets having been stored in the receiving buffer by their number, hold with a reference value for the number of received packets, which is determined beforehand, and cause the reproducing means to start data reproduction when the number of packets exceeds the reference value; the control means is adapted to monitor a state of the receiving buffer and discard, when the data in the buffer exceeds a predetermined buffer size, a fixed amount of data from the buffer; and the control means is adapted to monitor the number of packets in the receiving buffer at intervals of a predetermined time period and when a result shows increase or decrease in the number of packets with time, the control means is operated to modify, based on the result, the receiving buffer size and the reference value for the number of received packets at which reproduction of data is started.

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33. (added) A recording medium holding a packet processing program for use in a packet processor for processing packets of data received via a network to reproduce the data, wherein the packet processor comprises a receiving buffer for storing received packets; a reproducing means for reproducing data of the packets based on a reproduction clock; and a control means for performing control so as to reproduce data without breaks, and

wherein the packet processing program is characterized in that the control means is adapted to control the packets having been stored in the receiving buffer by their number, hold with a reference value for the number of received packets, which is determined beforehand, and cause the reproducing means to start data reproduction when the number of packets exceeds the reference value; the control means is adapted to monitor a state of the receiving buffer and modify, when the data in the buffer is used up, the reference value for the number of received packet at which reproduction of data is started, upon a next reproduction of data; and the control means is adapted to monitor the number of packets in the receiving buffer at intervals of a predetermined time period and when a result shows increase or decrease in the number of packets with time, the control means is operated to modify, based on the result, a receiving buffer size and the reference value for the number of received packets at which reproduction



of data is started.

34. (added) A recording medium holding a packet processing program for use in a packet processor for processing packets of data received via a network to reproduce the data, wherein the packet processor comprises a receiving buffer for storing received packets; a reproducing means for reproducing data of the packets based on a reproduction clock; and a control means for performing control so as to reproduce data without breaks, and

wherein the packet processing program is characterized in that the control means is adapted to control the packets having been stored in the receiving buffer by their number, hold with a reference value for the number of received packets, which is determined beforehand, and cause the reproducing means to start data reproduction when the number of packets exceeds the reference value; the control means is adapted to monitor a state of the receiving buffer and modify, when the data in the buffer exceeds a predetermined buffer size, the size of the receiving buffer; and the control means is adapted to monitor the number of packets in the receiving buffer at intervals of a predetermined time period and when a result shows increase or decrease in the number of packets with time, the control means is operated to modify, based on the result, the receiving buffer size and the reference

value for the number of received packets at which reproduction of data is started.

35. (added) A recording medium holding a packet processing program for use in a packet processor for processing packets of data received via a network to reproduce the data, wherein the packet processor comprises a receiving buffer for storing received packets; a reproducing means for reproducing data of the packets based on a reproduction clock; and a control means for performing control so as to reproduce data without breaks, and

wherein the packet processing program is characterized in that the control means is adapted to control the packets having been stored in the receiving buffer by their number, hold with a reference value for the number of received packets, which is determined beforehand, and cause the reproducing means to start data reproduction when the number of packets exceeds the reference value; the control means is adapted to monitor a state of the receiving buffer and collect, when the data in the buffer is used up, data up to a level corresponding to the reference value before causing the reproducing means to restart data reproduction; and the control means is adapted to monitor the number of packets in the receiving buffer at intervals of a predetermined time period and when a result shows increase or decrease in the

number of packets with time, the control means is operated to modify, based on the result, a clock for data reproduction.

36. (added) A recording medium holding a packet processing program for use in a packet processor for processing packets of data received via a network to reproduce the data, wherein the packet processor comprises a receiving buffer for storing received packets; a reproducing means for reproducing data of the packets based on a reproduction clock; and a control means for performing control so as to reproduce data without breaks, and

wherein the packet processing program is characterized in that the control means is adapted to control the packets having been stored in the receiving buffer by their number, hold with a reference value for the number of received packets, which is determined beforehand, and cause the reproducing means to start data reproduction when the number of packets exceeds the reference value; the control means is adapted to monitor a state of the receiving buffer and discard, when the data in the buffer exceeds a predetermined buffer size, a fixed amount of data from the buffer; and the control means is adapted to monitor the number of packets in the receiving buffer at intervals of a predetermined time period and when a result shows increase or decrease in the number of packets with time, the control means is operated to modify, based

on the result, a clock for data reproduction.

37. (added) A recording medium holding a packet processing  
program for use in a packet processor for processing packets  
of data received via a network to reproduce the data, wherein  
the packet processor comprises a receiving buffer for storing  
received packets; a reproducing means for reproducing data  
of the packets based on a reproduction clock; and a control  
means for performing control so as to reproduce data without  
breaks, and

wherein the packet processing program is characterized  
in that the control means is adapted to control the packets  
having been stored in the receiving buffer by their number,  
hold with a reference value for the number of received packets,  
which is determined beforehand, and cause the reproducing  
means to start data reproduction when the number of packets  
exceeds the reference value; the control means is adapted  
to monitor a state of the receiving buffer and modify, when  
the data in the buffer is used up, the reference value for  
the number of received packet at which reproduction of data  
is started, upon a next reproduction of data; and the control  
means is adapted to monitor the number of packets in the  
receiving buffer at intervals of a predetermined time period  
and when a result shows increase or decrease in the number  
of packets with time, the control means is operated to modify,

based on the result, a clock for data reproduction.

38. (added) A recording medium holding a packet processing  
program for use in a packet processor for processing packets  
of data received via a network to reproduce the data, wherein  
the packet processor comprises a receiving buffer for storing  
received packets; a reproducing means for reproducing data  
of the packets based on a reproduction clock; and a control  
means for performing control so as to reproduce data without  
breaks, and

wherein the packet processing program is characterized  
in that the control means is adapted to control the packets  
having been stored in the receiving buffer by their number,  
hold with a reference value for the number of received packets,  
which is determined beforehand, and cause the reproducing  
means to start data reproduction when the number of packets  
exceeds the reference value; the control means is adapted  
to monitor a state of the receiving buffer and modify, when  
the data in the buffer exceeds a predetermined buffer size,  
the size of the receiving buffer; and the control means is  
adapted to monitor the number of packets in the receiving  
buffer at intervals of a predetermined time period and when  
a result shows increase or decrease in the number of packets  
with time, the control means is operated to modify, based  
on the result, a clock for data reproduction.

39. (added) A recording medium holding a packet processing program for use in a packet processor for processing packets of data received via a network to reproduce the data, wherein the packet processor comprises a receiving buffer for storing received packets; a reproducing means for reproducing data of the packets based on a reproduction clock; and a control means for performing control so as to reproduce data without breaks, and

wherein the packet processing program is characterized in that the control means is adapted to control the packets having been stored in the receiving buffer by their number, hold with a reference value for the number of received packets, which is determined beforehand, and cause the reproducing means to start data reproduction when the number of packets exceeds the reference value; the control means is adapted to monitor a state of the receiving buffer and modify, when the data in the buffer is used up, the reference value for the number of received packet at which reproduction of data is started, upon a next reproduction of data; and the control means is adapted to record the modifications of the reference value for the number of received packets or a receiving buffer size, continuously from a start of a communication, and if modifications of one type successively occur, the control means is operated to modify, based on the result, the receiving

buffer size and the reference value for the number of received packets at which reproduction of data is started.

40. (added) A recording medium holding a packet processing program for use in a packet processor for processing packets of data received via a network to reproduce the data, wherein the packet processor comprises a receiving buffer for storing received packets; a reproducing means for reproducing data of the packets based on a reproduction clock; and a control means for performing control so as to reproduce data without breaks, and

wherein the packet processing program is characterized in that the control means is adapted to control the packets having been stored in the receiving buffer by their number, hold with a reference value for the number of received packets, which is determined beforehand, and cause the reproducing means to start data reproduction when the number of packets exceeds the reference value; the control means is adapted to monitor a state of the receiving buffer and modify, when the data in the buffer exceeds a predetermined buffer size, the size of the receiving buffer; and the control means is adapted to record the modifications of the reference value for the number of received packets or the receiving buffer size, continuously from a start of a communication, and if modifications of one type successively occur, the control

means is operated to modify, based on the result, the receiving buffer size and the reference value for the number of received packets at which reproduction of data is started.

5        41. (added)        A recording medium holding a packet processing program for use in a packet processor for processing packets of data received via a network to reproduce the data, wherein the packet processor comprises a receiving buffer for storing received packets; a reproducing means for reproducing data  
10       of the packets based on a reproduction clock; and a control means for performing control so as to reproduce data without breaks, and

         wherein the packet processing program is characterized  
15       in that the control means is adapted to control the packets having been stored in the receiving buffer by their number, hold with a reference value for the number of received packets, which is determined beforehand, and cause the reproducing means to start data reproduction when the number of packets exceeds the reference value; the control means is adapted  
20       to monitor a state of the receiving buffer and modify, when the data in the buffer is used up, the reference value for the number of received packet at which reproduction of data is started, upon a next reproduction of data; and the control means is adapted to record the modifications of the reference  
25       value for the number of received packets or a receiving buffer



size, continuously from a start of a communication, and if modifications of one type successively occur, the control means is adapted to modify, based on the result, a clock for data reproduction.

5  
42. (added) A recording medium holding a packet processing program for use in a packet processor for processing packets of data received via a network to reproduce the data, wherein the packet processor comprises a receiving buffer for storing  
10 received packets; a reproducing means for reproducing data of the packets based on a reproduction clock; and a control means for performing control so as to reproduce data without breaks, and

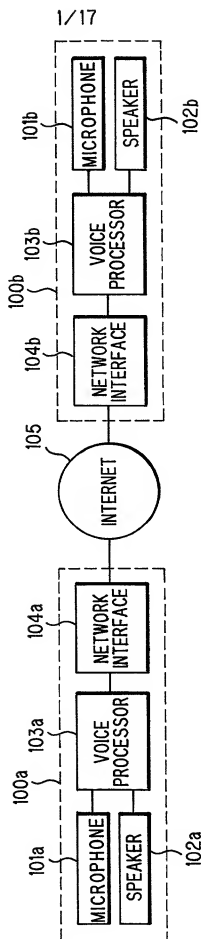
15 wherein the packet processing program is characterized in that the control means is adapted to control the packets having been stored in the receiving buffer by their number, hold with a reference value for the number of received packets, which is determined beforehand, and cause the reproducing means to start data reproduction when the number of packets  
20 exceeds the reference value; the control means is adapted to monitor a state of the receiving buffer and modify, when the data in the buffer exceeds a predetermined buffer size, the size of the receiving buffer; and the control means is adapted to record the modifications of the reference value  
25 for the number of received packets or the receiving buffer

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size, continuously from a start of a communication, and if modifications of one type successively occur, the control means is adapted to modify, based on the result, a clock for data reproduction.

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FIG. 1



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FIG. 2

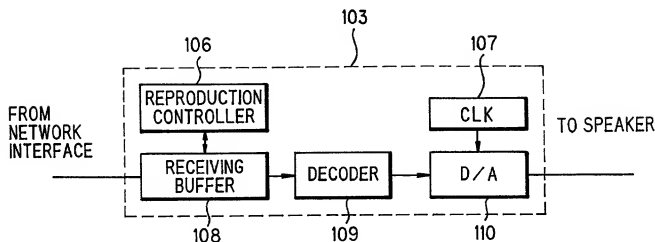
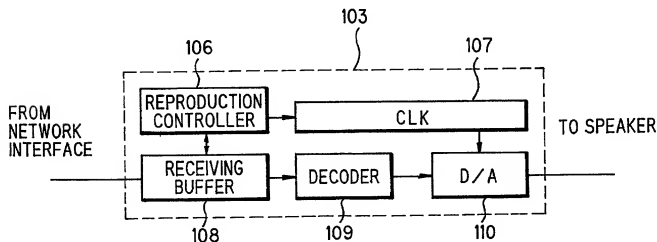


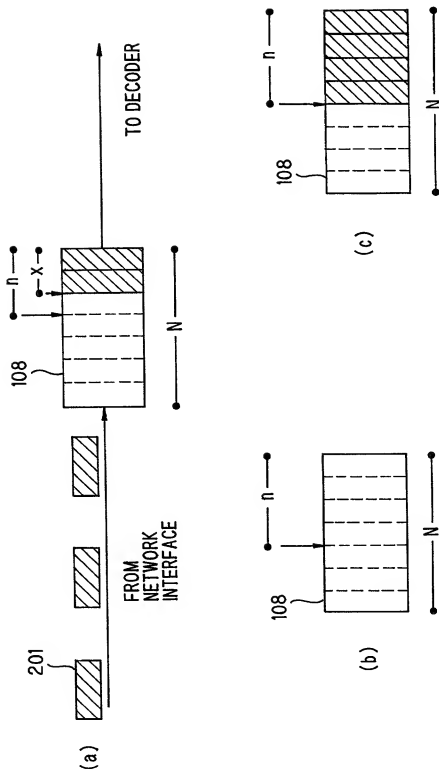
FIG. 3



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FIG. 4

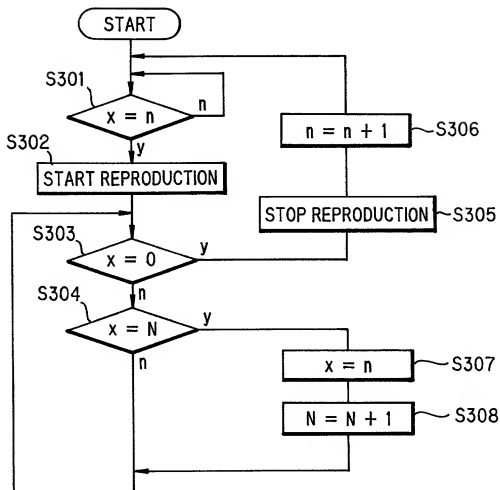


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FIG. 5

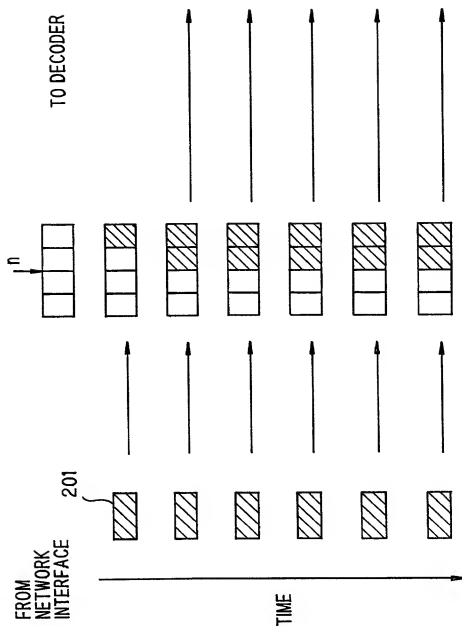


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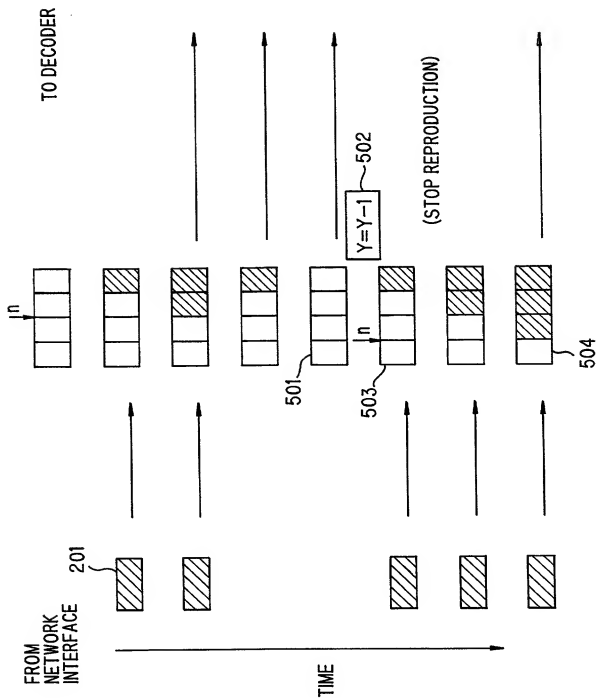
FIG. 6



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FIG. 7

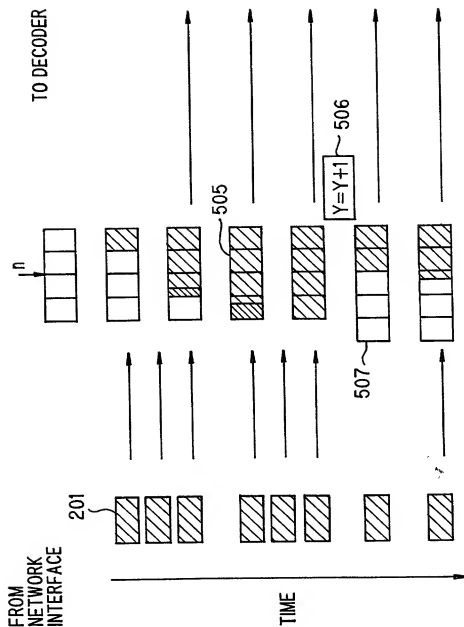




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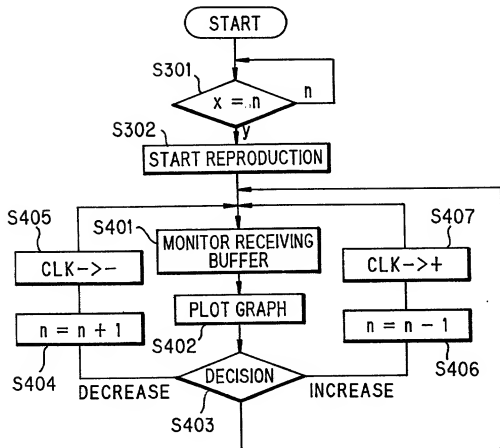
FIG. 8



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FIG. 9

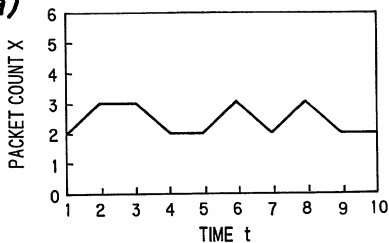


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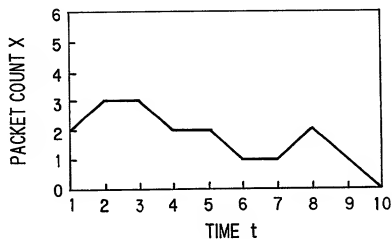
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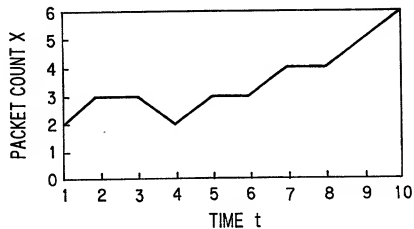
FIG. 10  
(a)



(b)



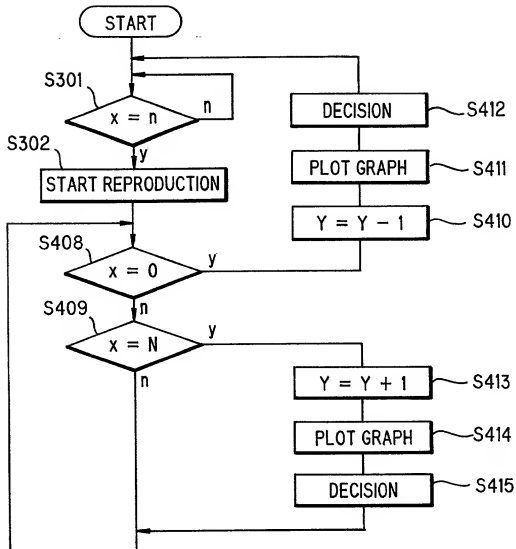
(c)



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FIG. 11

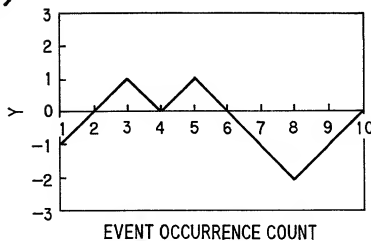


10031200.011502

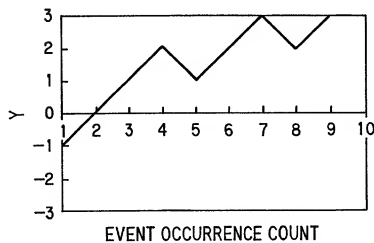
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FIG. 12  
(a)

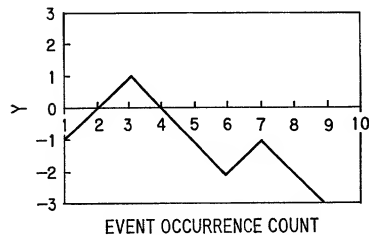
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(b)



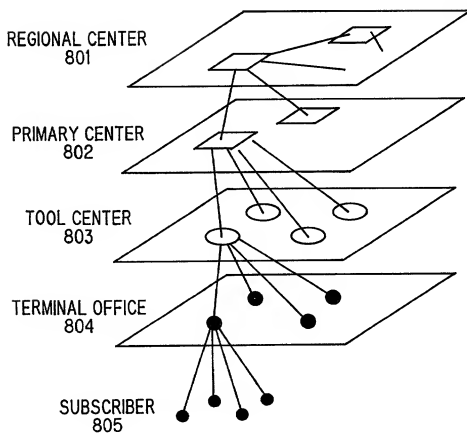
(c)



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**FIG. 13**

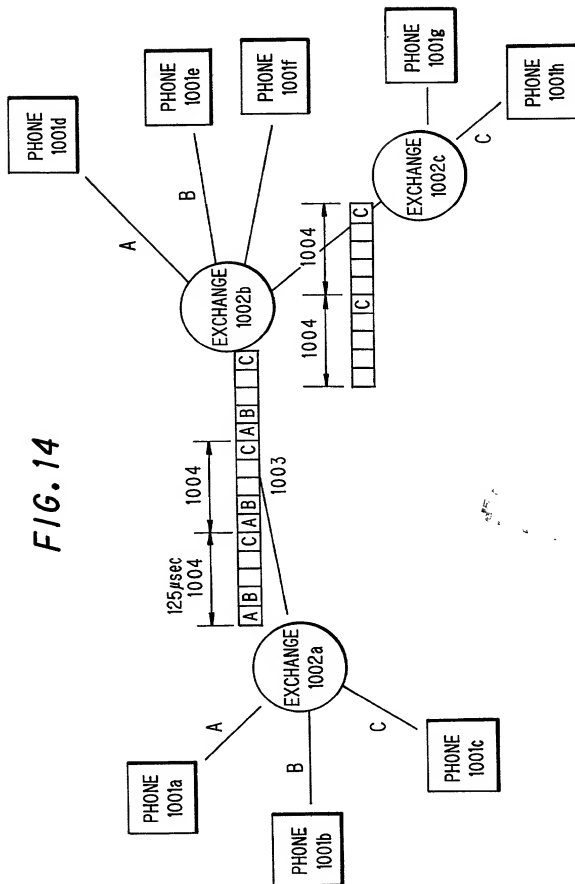


10031200.01502

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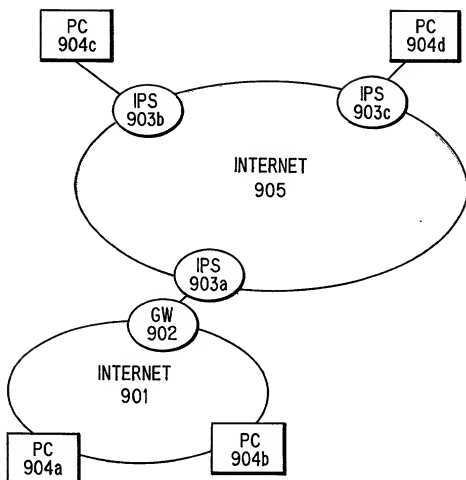
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FIG. 14



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FIG. 15

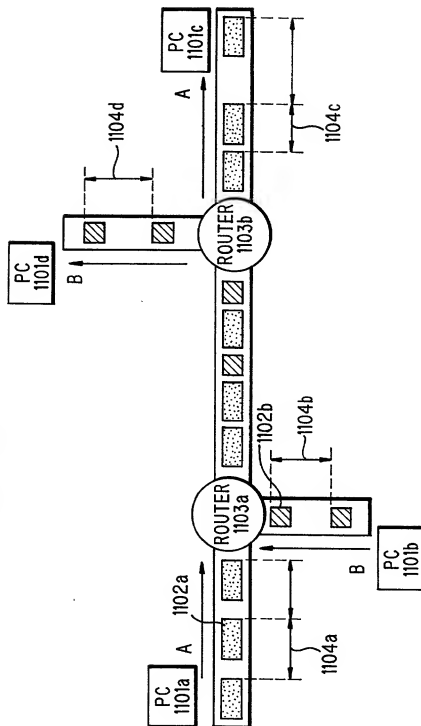




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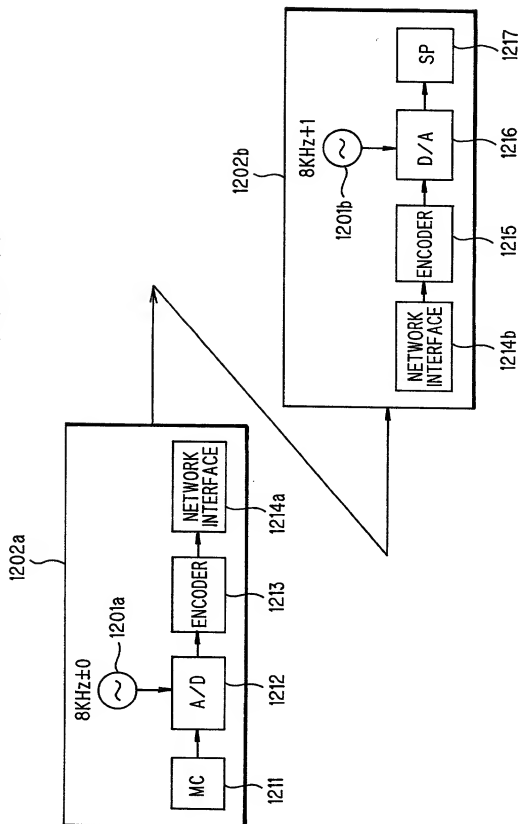
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FIG. 16



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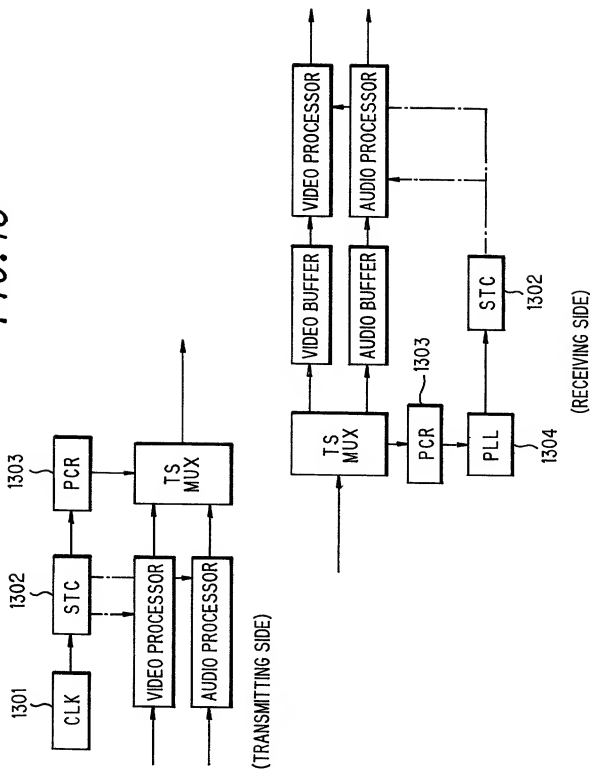
FIG. 17



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FIG. 18



Nixon & Vanderhye P.C. (10/99)  
(Domestic Non-Assigned/Foreign)

**RULE 63 (37 C.F.R. 1.63)**  
**DECLARATION AND POWER OF ATTORNEY**  
**FOR PATENT APPLICATION**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated below next to my name, and I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

**PACKET PROCESSOR AND RECORDING MEDIUM WITH PACKET PROCESSING PROGRAMS THEREON**  
the specification of which (check applicable box(es)):

☐ is attached hereto  
☐ was filed on \_\_\_\_\_ as U.S. Application Serial No. \_\_\_\_\_ (Atty Dkt. No.)  
☒ was filed as PCT International application No. PCT/JF00/05167 on August 2, 2000  
and (if applicable to U.S. or PCT application) was amended on February 26, 2001

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with 37 C.F.R. 1.56. I hereby claim foreign priority benefits under 35 U.S.C. 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed or, if no priority is claimed, before the filing date of this application.

Priority Foreign Application(s)	Patent Application	Country	Date/Month/Year Filed
Application Number			
He1 11-220191		Japan	3/August/1999

I hereby claim the benefit under 35 U.S.C. §119(e) of any United States provisional application(s) listed below.

Application Number	Date/Month/Year Filed

I hereby claim the benefit under 35 U.S.C. 120/365 of all prior United States and PCT International applications listed above or below and, insofar as the subject matter of each of the claims of this application is not disclosed in such prior applications in the manner provided by the first paragraph of 35 U.S.C. 112, I acknowledge the duty to disclose material information as defined in 37 C.F.R. 1.56 which occurred between the filing date of the prior applications and the national or PCT International filing date of this application.

Prior U.S./PCT Application(s): Application Serial No.	Date/Month/Year Filed	Status: patented pending, abandoned
PCT/JF00/05167	2/August/2000	

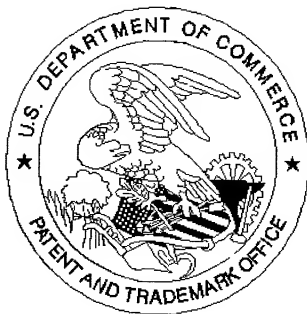
I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon. And on behalf of the owner(s) hereof, I hereby appoint **NIXON & VANDERHYE P.C., 1100 North Glebe Rd., 8th Floor, Arlington, VA 22201-4714, telephone number (703) 816-4000** (to whom all communications are to be directed), and the following attorneys thereof (of the same address) individually and collectively owner's/owners' attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith and with the resulting patent. I also authorize Nixon & Vanderhye to delete any attorney names/numbers no longer with the firm and to act and rely solely on instructions directly communicated from the person, assignee, attorney, firm, or other organization sending instructions to Nixon & Vanderhye on behalf of the owner(s).

1. Inventor's Signature: Taro Nakabayashi Date: December 27, 2001  
Inventor: Taro NAKABAYASHI Japan  
(first) (last) (citizenship)  
Residence: (city) Suginami-ku (state/country) Tokyo, Japan JPN  
Post Office Address: 2-14-17-103, Miyamee, Suginami-ku, Tokyo, Japan  
(Zip Code) 168-0081

2. Inventor's Signature: \_\_\_\_\_ Date: \_\_\_\_\_  
Inventor: \_\_\_\_\_ (first) MI (last) (citizenship)  
Residence: (city) \_\_\_\_\_ (state/country) \_\_\_\_\_  
Post Office Address: \_\_\_\_\_  
(Zip Code) \_\_\_\_\_

FOR ADDITIONAL INVENTORS, check box ☐ and attach sheet with same information and signature and date for each.

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